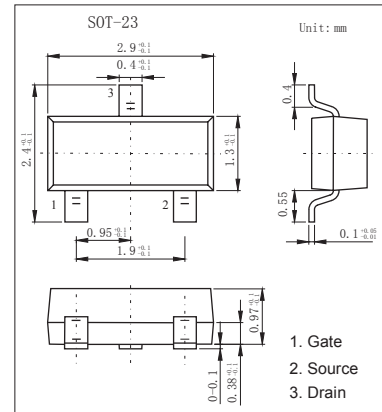
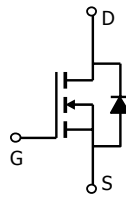


# N-Channel Enhancement MOSFET

## LJ3404

### ■ Features

- $V_{DS} (V) = 30V$
- $I_D = 5.8 A (V_{GS} = 10V)$
- $R_{DS(ON)} < 28 m\Omega (V_{GS} = 10V)$
- $R_{DS(ON)} < 43 m\Omega (V_{GS} = 4.5V)$



### ■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter		Symbol	Rating	Unit
Drain-Source Voltage		$V_{DS}$	30	V
Gate-Source Voltage		$V_{GS}$	$\pm 20$	
Continuous Drain Current	$T_a = 25^\circ C$	$I_D$	5.8	A
	$T_a = 100^\circ C$		4.9	
Pulsed Drain Current		$I_{DM}$	20	
Power Dissipation	$T_a = 25^\circ C$	$P_D$	1.4	W
	$T_a = 70^\circ C$		1	
Thermal Resistance.Junction- to-Ambient	$t \leq 5sec$	$R_{thJA}$	90	$^\circ C/W$
	Steady State		125	
Thermal Resistance.Junction- to-Lead		$R_{thJL}$	60	
Junction Temperature		$T_J$	150	$^\circ C$
Storage Temperature Range		$T_{stg}$	-55 to 150	

# N-Channel Enhancement MOSFET

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**■ Electrical Characteristics Ta = 25°C**

Parameter	Symbol	Testconditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V <sub>DSS</sub>	I <sub>D</sub> =250 μ A, V <sub>GS</sub> =0V	30			V
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>D</sub> =24V, V <sub>GS</sub> =0V			1	μ A
		V <sub>D</sub> =24V, V <sub>GS</sub> =0V, T <sub>J</sub> =55°C			5	
Gate-Body leakage current	I <sub>GSS</sub>	V <sub>D</sub> =0V, V <sub>GS</sub> =±20V			100	nA
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>D</sub> =V <sub>GS</sub> I <sub>D</sub> =250 μ A	1	1.9	3	V
On state drain current	I <sub>D(ON)</sub>	V <sub>GS</sub> =4.5V, V <sub>D</sub> =5V	20			A
Static Drain-Source On-Resistance	R <sub>DS(ON)</sub>	V <sub>GS</sub> =10V, I <sub>D</sub> =5.8A		22.5	28	m Ω
		V <sub>GS</sub> =10V, I <sub>D</sub> =5.8A T <sub>J</sub> =125°C		31.3	38	
		V <sub>GS</sub> =4.5V, I <sub>D</sub> =5.0A		34.5	43	m Ω
Forward Transconductance	g <sub>FS</sub>	V <sub>D</sub> =5V, I <sub>D</sub> =5.8A	10	14.5		S
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =1A		0.76	1	V
Maximum Body-Diode Continuous Current	I <sub>S</sub>				2.5	A
Reverse Transfer Capacitance	C <sub>iss</sub>			680	820	pF
Gate resistance	C <sub>oss</sub>	V <sub>GS</sub> =0V, V <sub>D</sub> =15V, f=1MHz		102		pF
Input Capacitance	C <sub>rss</sub>			77		pF
Output Capacitance	R <sub>g</sub>	V <sub>GS</sub> =0V, V <sub>D</sub> =0V, f=1MHz		3	3.6	Ω
Total Gate Charge (10V)	Q <sub>g</sub>	V <sub>GS</sub> =10V, V <sub>D</sub> =15V, I <sub>D</sub> =5.8A		13.88	17	nC
Total Gate Charge (4.5V)	Q <sub>g</sub>			6.78	8.1	nC
Gate Source Charge	Q <sub>gs</sub>			1.8		nC
Gate Drain Charge	Q <sub>gd</sub>			3.12		nC
Turn-On Rise Time	t <sub>D(on)</sub>				4.6	6.5
Turn-Off DelayTime	t <sub>r</sub>	V <sub>GS</sub> =10V, V <sub>D</sub> =15V, R <sub>L</sub> =2.7 Ω, R <sub>GEN</sub> =3 Ω		3.8	5.7	ns
Turn-Off Fall Time	t <sub>D(off)</sub>			20.9	30	ns
Turn-On DelayTime	t <sub>f</sub>			5	7.5	ns
Body Diode Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> =5.8A, di/dt=100A/μ s		16.1	21	ns
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	I <sub>F</sub> =5.8A, di/dt=100A/μ s		7.4	10	nC

**■ Marking**

Marking	A4*
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# N-Channel Enhancement MOSFET

## LJ3404

### Typical Characteristics

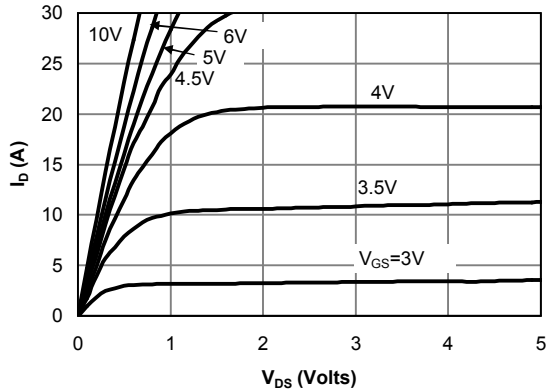


Fig 1: On-Region Characteristics

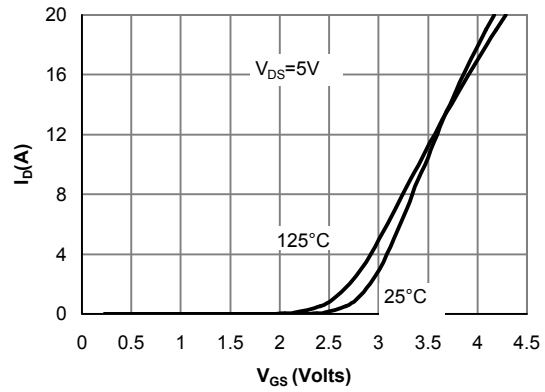


Figure 2: Transfer Characteristics

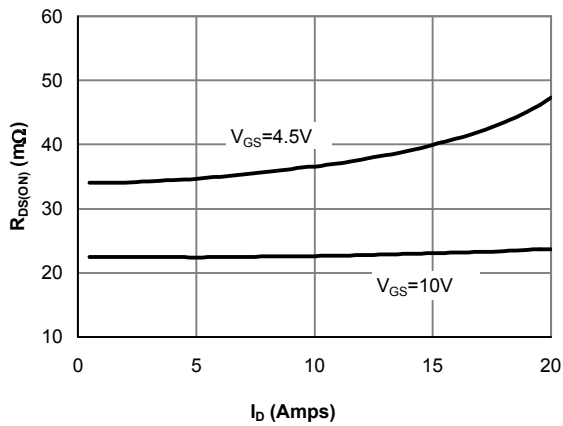


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

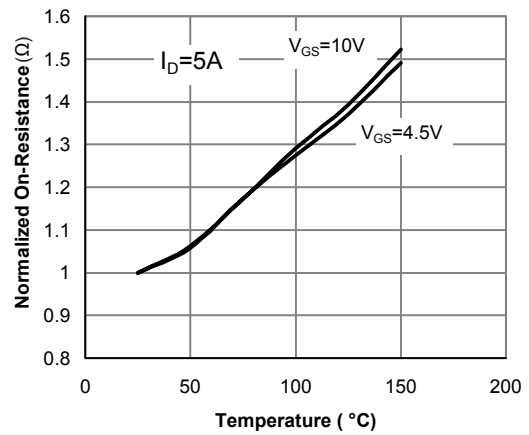


Figure 4: On-Resistance vs. Junction Temperature

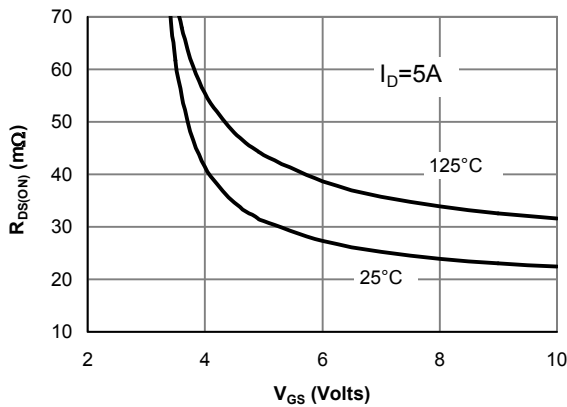


Figure 5: On-Resistance vs. Gate-Source Voltage

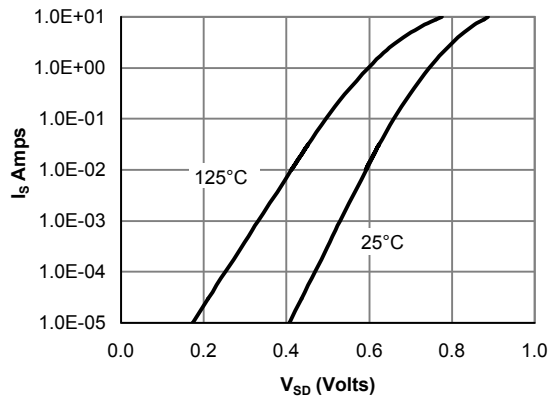


Figure 6: Body diode characteristics

# N-Channel Enhancement MOSFET

## LJ3404

### Typical Characteristics

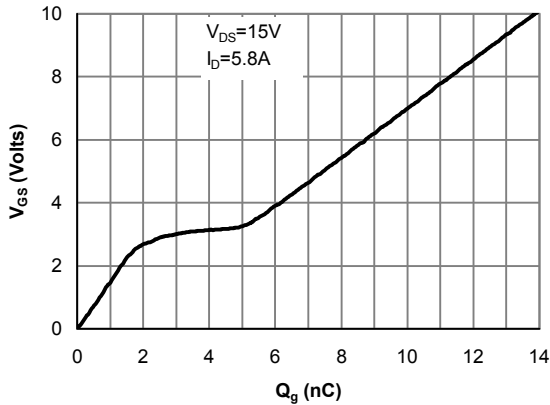


Figure 7: Gate-Charge characteristics

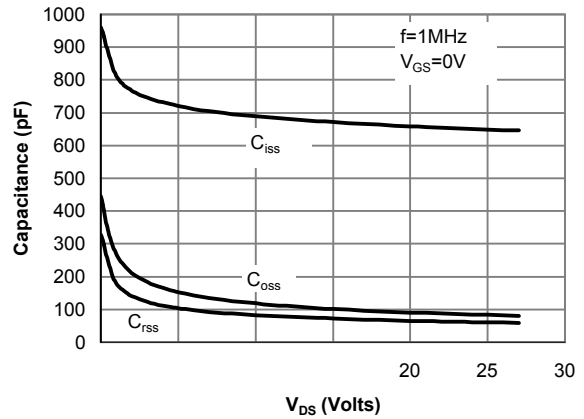


Figure 8: Capacitance Characteristics

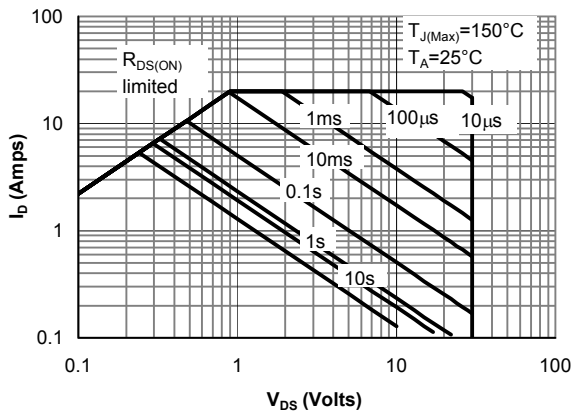


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

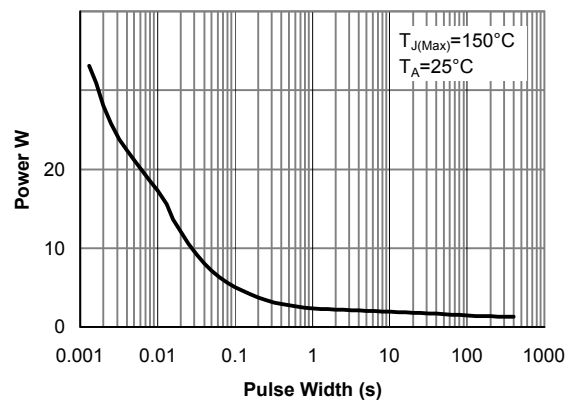


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

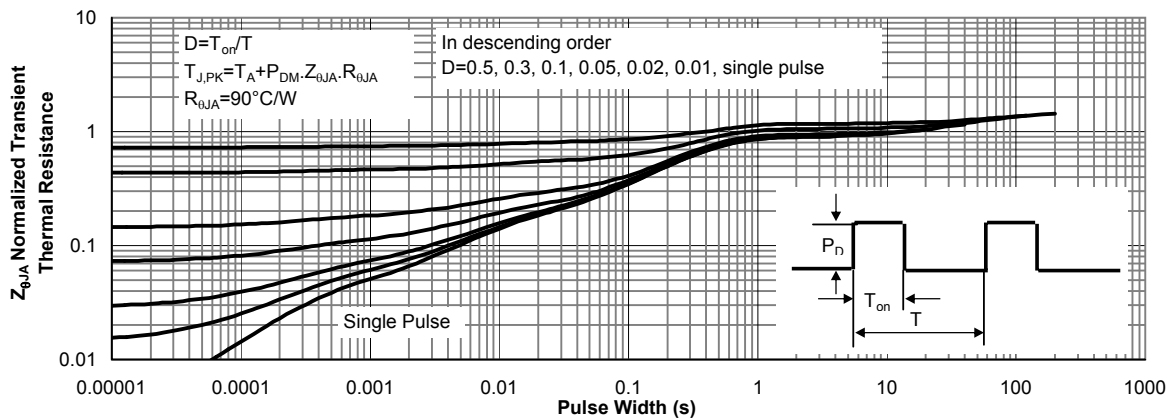


Figure 11: Normalized Maximum Transient Thermal Impedance