

SILICON BRIDGE RECTIFIER

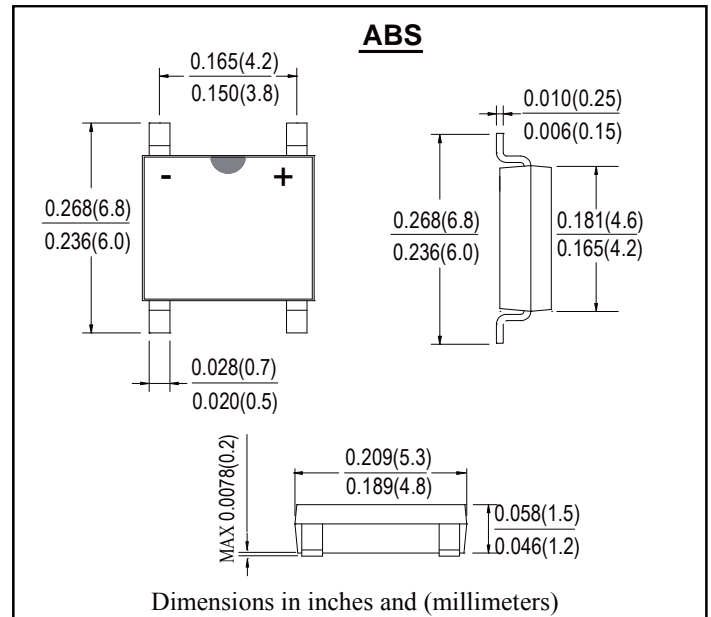
REVERSE VOLTAGE : 200 --- 1000 V CURRENT: 2.0A

Features

- Glass passivated die construction
- Low forward voltage drop
- High current capability
- High surge current capability
- Designed for surface mount application
- Plastic material-UL flammability 94V-O

Mechanical Data

- Case: SOPA-4, molded plastic
- Terminals: plated leads solderable per MIL-STD-202, Method 208
- Polarity: as marked on case
- Mounting position: Any



Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified. Single Phase, half wave, 60Hz, resistive or inductive load. For capacitive load, derate current by 20%.

| TYPE NUMBER | SYMBOL | ABS202 | ABS204 | ABS206 | ABS208 | ABS210 | UNITS |
|---|-----------------|------------|--------|--------|--------|--------|-------|
| Peak Repetitive Reverse Voltage | V_{RM} | 200 | 400 | 600 | 800 | 1000 | V |
| Working Peak Reverse Voltage | V_{RWM} | | | | | | |
| DC Blocking Voltage | V_{DC} | | | | | | |
| RMS Reverse Voltage | V_{RMS} | 140 | 280 | 420 | 560 | 700 | V |
| Average Rectified Output Current @TA=65°C | I_o | 2.0 | | | | | A |
| Non-Repetitive Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method) | I_{FSM} | 60 | | | | | A |
| Forward Voltage per element @IF=2.0A | V_{FM} | 1.1 | | | | | V |
| Peak Reverse Current @TA=25°C At Rated DC Blocking Voltage @TA=125°C | I_R | 5.0 500 | | | | | uA |
| Typical Thermal Resistance per leg | $R_{\theta JA}$ | 62.5 | | | | | °C/W |
| | $R_{\theta JL}$ | 25 | | | | | |
| Operating and Storage Temperature Range | T_J, T_{STG} | -55to+150 | | | | | °C |

RATINGS AND CHARACTERISTIC CURVES

FIG.1-FORWARD CURRENT DERATING CURVE

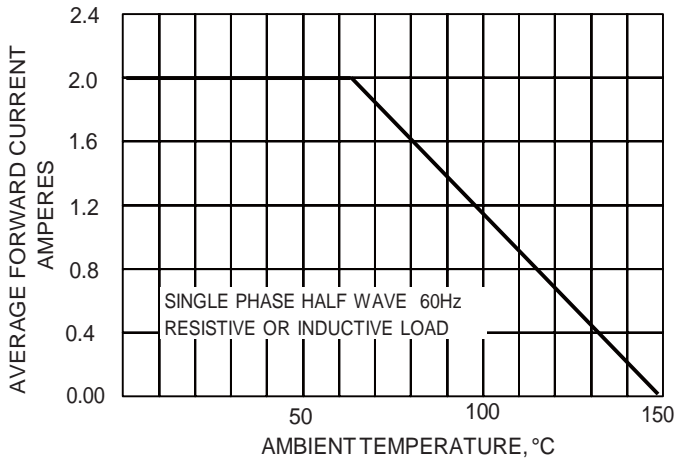


FIG.2-TYPICAL FORWARD CHARACTERISTICS

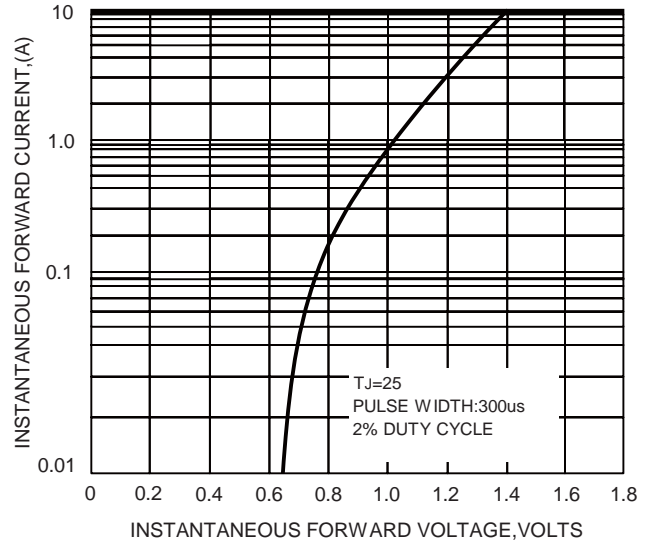


FIG.3-MXIMUM NON-REPETITIVE SURGE CURRENT

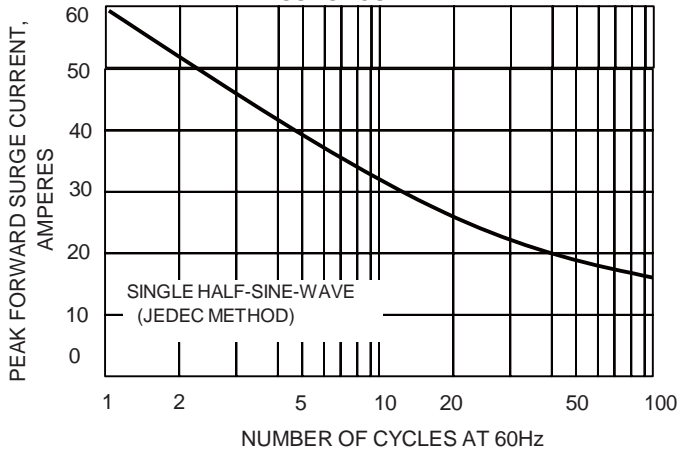


FIG.4-TYPICAL JUNCTION CAPACITANCE

